

## REMARKS

1. New Oath and Declaration is requested for replacement an original Oath and Declaration because the original Oath and Declaration is missing the date and the signature of the inventors:

The new Oath and Declaration could not be prepared in time because one of the inventors had gone on a business trip. The new Oath and Declaration will be sent as soon as possible.

2. Rejection of claims 1-8 under 35 U.S.C. 102(a) as being anticipated by APPLICANT'S PRIOR ART:

Claim 5 is amended to incorporate the limitation that "one of the digit circuits (34-39) with least signal variation among the bits (34) of the digital voltage signal for outputting the transformed analog voltage signal is closest to the output module (42)" as shown in Fig.3 of the present application, and is amended to remove the phrase "each digit circuit corresponding to a bit of the digital voltage signal" that is considered to be redundant. Accordingly, claim 1 is amended, while the apparatus discussed above would perform the claim 1. No new matter is introduced.

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Referring to Fig.1, rows 23-28 of Page 2, and rows 1-7 of Page 3 of the present application, the applicant's prior art merely mentions that "the digital-to-analog converter includes a receiving circuit 12, six digit circuits 14-19, a reference circuit 20, and an output module 22. The receiving circuit 12 is used for receiving the 6-bit digital voltage signal, and for respectively being electrically connected to

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the six digit circuits 14-19. The reference circuit 20 is used to provide nine different reference voltages for the six digit circuits 14-19. The nine different reference voltages are respectively 0V, 0.5V, 1V, 1.5V, 2V, 2.5V, 3V, 3.5V, and 4V.

5 The six digit circuits 14-19 include a first digit circuit 14 to a sixth digit circuit 19, and the six digit circuits 14-19 (the first digit circuit 14 to the sixth digit circuit 19) respectively correspond to six bits of the digital voltage signal (a first bit to a sixth bit). For example, the first

10 digit circuit 14 corresponds to the first bit of the digital voltage signal."

The applicant's prior art fails to teach the limitation that "one of the digit circuits with least signal variation among

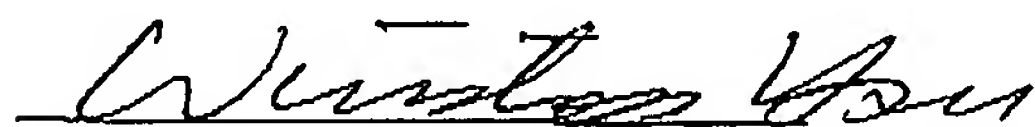
15 the bits of the digital voltage signal for outputting the transformed analog voltage signal is closest to the output module".

Reconsideration of amended claims 1 and 5 is politely

20 requested. Claims 2-4 and 6-8 are dependent on claims 1 and 5 and should be allowed if claims 1 and 5 are allowed.

Sincerely,

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Winston Hsu, Patent Agent No. 41,526

30 P.O. BOX 506  
Merrifield, VA 22116  
U.S.A.

e-mail : winstonhsu@naipo.com.tw

(Please contact me by e-mail if you need a telephone communication and I will return your call promptly.)

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